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signals) to digital data. ADCs may be used for example, in radio frequency (RF) transmitters and/or receivers in which the analog signal may be RF signals. The ADCs may convert analog signals to digital data, and may be used to transfer the digital data in a networked system. An ADC may convert the analog signal to a digital signal using a conversion pipeline. The conversion pipeline may include a plurality of stages, including a first stage for receiving the analog signal, a second stage for amplifying the analog signal, and a plurality of subsequent stages for analog-to-digital conversion of the analog signal. The analog-to-digital conversion may include an ADC module that may convert the analog signal to a digital signal using successive approximation. The ADC module may iteratively compare the analog signal to one or more reference signals, and may select a reference signal having a value most similar to the analog signal. The ADC module may then generate a digital signal based on the selected reference signal. The successive approximation may be performed until the digital signal corresponds to a digital code. ADCs may be implemented as an integrated circuit (IC). The IC may include a die on which the ADC is formed. The IC may be a multi-chip module, a hybrid module, and/or a monolithic IC. The ADC may include a plurality of signal processing circuits, including a signal processing circuit that may operate as an ADC module. The inventors have recognized several challenges relating to the above-described process. For example, the inventors have recognized that implementing a plurality of ADC modules in a single IC may be costly, as each ADC module may consume significant area in the IC. Additionally, the inventors have recognized that using an ADC module for each channel of an ADC may be inefficient. As a result, the inventors have recognized the need for improved ADC modules and techniques that may increase efficiency and reduce circuit area. In

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